

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Christophe MALEVILLE et al.

SURFACE OF A SEMICONDUCTOR LAYER OF

Confirmation No.5090

Application No.: 10/808,288

Group Art Unit: 2812

Filing Date: March 25, 2004

Examiner:

For: METHOD FOR PREPARING A BONDING

Atty. Docket No.: 4717-10500

A WAFER

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

Pursuant to applicants' duty of disclosure under 37 C.F.R. 1.56, enclosed are copies of (2) references for the Examiner's review and consideration. These references were cited in the Preliminary Search Report for this application and a copy is enclosed.

These references are listed on the enclosed Form PTO-1449. It is respectfully requested that these references be made of record in this application by the Examiner's completion and return of the PTO Form 1449.

No fee is believed to be due for the filing of this statement as it is being submitted prior to an initial office action for this application. Should any fees be required, however, please charge such fees to Winston & Strawn LLP Deposit Account No. 50-1814.

Respectfully submitted,

Date:

Allan A Fanuc

(Reg. No. 30.256)

WINSTON & STRAWN LLP CUSTOMER NO. 28765

(212) 294-3311

Enclosures

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